

# Multi-modal Machine Learning for Hardening Firmware Binaries



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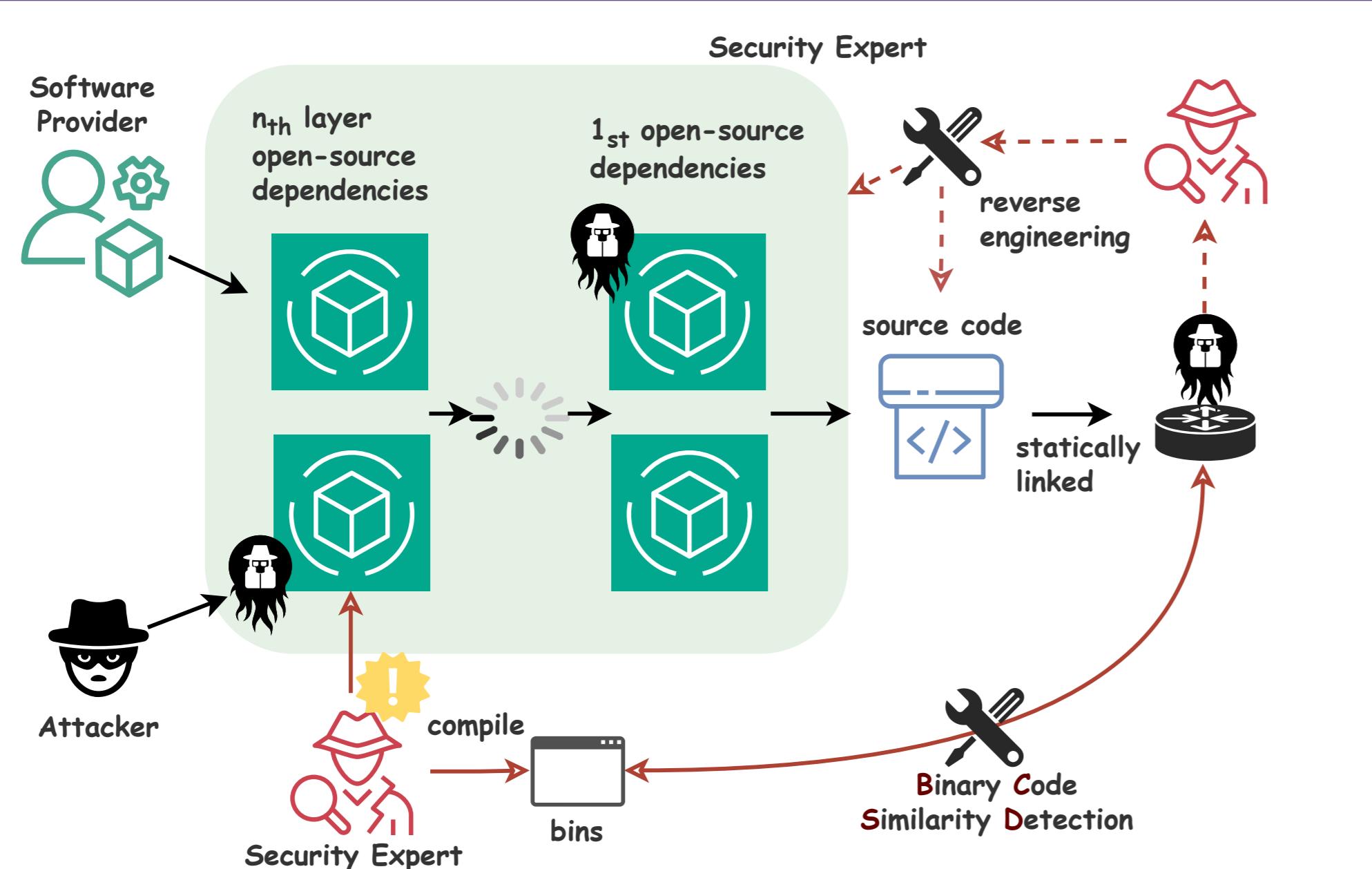
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# CONVEY

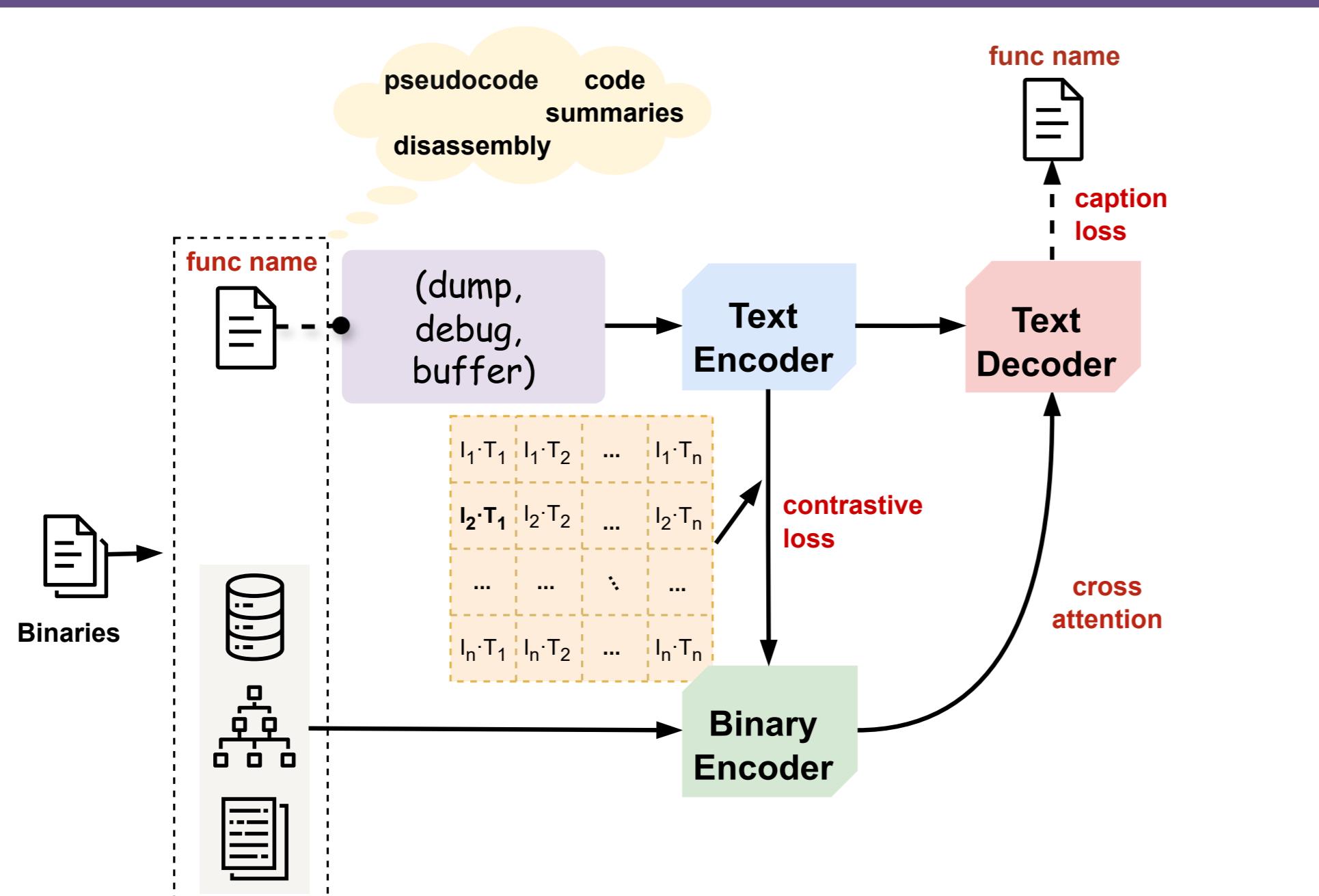


# Software Supply Chain



The heavy reliance on **third-party libraries** in embedded firmware heightens software supply chain security risks. **BCSD** addresses known vulnerabilities, while **reverse engineering** reveals unknown ones.

# MMML for Binary Code



- Align binary encoding with function names in latent space to generalize to zero-shot learning.
  - Reconstruct high-level structures from binaries to assist in reverse engineering.
  - Generalize to binaries across domains and different downstream tasks

# Optimization Levels

Binaries compiled with different configurations vary significantly. For instance, in O0 optimization, call arguments are pushed onto the stack, whereas they are optimized in O1.

```
static void* default_bzalloc ( void* opaque, Int32 items, Int32 size )
{
    void* v = malloc ( items * size );
    return v;
}
```

```

.text:00000000004066D0 default_bzalloc proc near
.text:00000000004066D0 var_18      = qword ptr -18h
.text:00000000004066D0 var_10      = dword ptr -10h
.text:00000000004066D0 var_C       = dword ptr -0Ch
.text:00000000004066D0 var_8       = qword ptr -8
...
.text:00000000004066D8          mov    [rbp+var_8], rdi
.text:00000000004066DC          mov    [rbp+var_C], esi
.text:00000000004066DF          mov    [rbp+var_10], edx
.text:00000000004066E2          mov    eax, [rbp+var_C]
.text:00000000004066E5          imul   eax, [rbp+var_10]
.text:00000000004066E9          movsxd rdi, eax     ; size
.text:00000000004066EC          call   _malloc
.text:00000000004066F1          mov    [rbp+var_18], rax
.text:00000000004066F5          mov    rax, [rbp+var_18]
...

```

a. a disassembly segment of default\_bzalloc (-O0)

```

.text:0000000000403EC0 default_bzalloc proc near
.text:0000000000403EC0 ; __ unwind {
.text:0000000000403EC0 push   rax
.text:0000000000403EC1 imul   esi, edx
.text:0000000000403EC4 movsxd rdi, esi
.text:0000000000403EC7 call   _malloc
.text:0000000000403ECC pop   rcx
.text:0000000000403ECD retn
.text:0000000000403ECD ; } // starts at 403EC0
.text:0000000000403ECD default_bzalloc endp

```

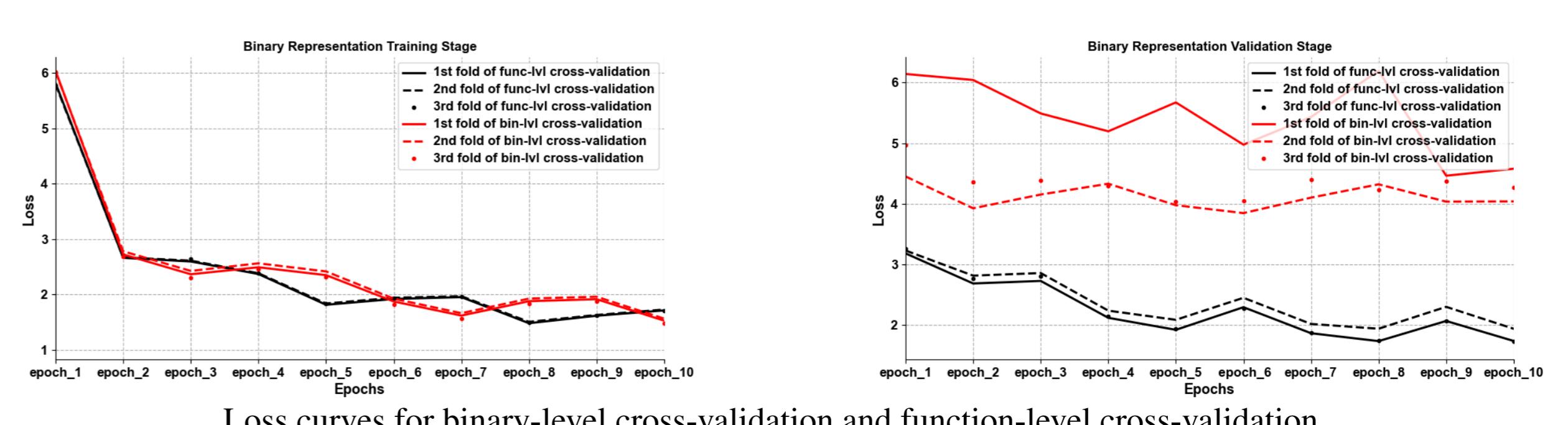
b. a disassembly segment of default\_bzalloc (-O1)

# Binary Distribution

In small-scale function name generation experiments, we observed significant differences in results based on the splitting strategy:

- F1 score averaged **0.6646** when splitting by functions
  - F1 score averaged **0.4708** when splitting by binaries

We also noticed poor generalization between binaries when evaluating other state-of-the-art approaches.



## References

- [1] A. Radford et al. ‘Learning transferable visual models from natural language supervision’. In: *International Conference on Machine Learning*, ICML, 2021, pp. 8748–8763.